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# Nanoscale

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### **ARTICLE**

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## **Tunable Schottky barrier height of Pt-CuO junction via triboelectric nanogenerator**

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Tuning Schottky barrier height is crucial to optimize the performance of Schottky junction device. Here, we demonstrate that the Schottky barrier height can be tuned by the voltage from triboelectric nanogenerator (TENG). Schottky barrier heights at both ends are increased after the treatment of the voltage generated by TENG. The electric field generated by impulse voltage of TENG drives the diffusion of the ionized oxygen vacancy in CuO nanowire, which induces the nonuniform distribution of the ionized oxygen vacancy. The oxygen vacancy positively charged accumulates at the contacted interface of Pt and CuO nanowire, it impels the conduction and valence bands to bend downwards. The Schottky barrier height is raised. A theoretical model based on the energy band diagram is proposed to explain this phenomenon. This method offers a simple and effective avenue to tune the Schottky barrier height. It opens up the possibility to develop the high Schottky sensor by tuning the Schottky barrier height.

#### **Introduction**

Schottky contact is an important role in virtually all microelectric fields. Schottky barrier height at the interface of metalsemiconductor controls the depletion width and the mechanism of carrier transport.<sup>1, 2</sup> Tuning Schottky barrier height can optimize the performance of Schottky devices. 3,4 By tuning Schottky barrier height, the detecting performance is significantly enhanced for photodetector,<sup>5-7</sup> gas sensor,<sup>8, 9</sup> biosensor,  $10-12$  chemical sensor,  $13$  strain sensor,  $14$  and so on.

Piezotronic effect as a dynamic adjusting method of Schottky barrier height is an effective way for piezoelectric semiconductors (including ZnO, GaN, ZnS, CdS, and so on) once a stress is applied.15-17 Then, the transport process of the carrier is modulated at the interface/junction.<sup>18</sup> Piezotronic effect has been utilized to develop the high-performance sensor based on Schottky contact.<sup>16</sup> The piezoelectric semiconductor with nanowire structure allows the large deformations before

fracture comparing with bulk materials. However, the risk of fracture is increased after multiple deformations in process of the experiment. Another dynamic adjusting method is the polarization of charged oxygen vacancy in ZnO nanowire (ntype) using triboelectric nanogenerator (TENG) with the properties of high voltage and low current.<sup>19-21</sup> The discovery that the Schottky barrier height of the Ag-ZnO junction is decreased after being treated by TENG is noticeable.<sup>5, 9, 11, 22</sup> Whereas the change of Schottky barrier height based on p-type nanowire semiconductor after being treated by TENG is still needed to investigate to understand this effect in-depth. CuO nanowire has been known as a p-type semiconductor that exhibits a narrow bandgap and other interesting properties  $23$ , <sup>24</sup>. CuO nanowire is a promising p-type semiconductor due to its easy fabrication, low cost, environmentally friendly nature, and easy preparation. Schottky junction based on CuO nanowire is widely applied in the field of photodetector, $25-27$  gas detection,<sup>28, 29</sup> chemical sensor,<sup>30</sup> memristors,<sup>31</sup> field-effect transistors,<sup>32</sup> and so on. ARTICLE<br> **Nanoscale Constitute On 13 September 2021.** Tunable Schottky barrier height of Pt-CuO junction via<br>
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> In this work, we chose CuO nanowire as a p-type semiconductor to prepare a device of the Schottky junction. The high voltage produced by TENG is used to modulate Schottky barrier height. The phenomenon that Schottky barrier height of Pt and CuO nanowire is increased after treatment by TENG. A

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theoretical model is proposed to explain this phenomenon. The discovery in this work is beneficial to understand the interaction mechanism between impulse voltage of TENG and Schottky barrier height. It provides an avenue to develop highperformance Schottky-contacted sensors.

#### **Experimental**

Materials preparation and characterization: CuO nanowires were synthesized by heating copper substrate in air according to the protocol of Jiang *et al..*<sup>33</sup> The surface morphology of CuO nanowire was obtained by scanning electron microscope (SEM, Hitachi SU8020). X-ray diffraction (XRD) pattern was used to get the information of phase structure by X-ray diffractometer (PANalytical X`Pert) with Cu kα source (λ=0.154 nm). Composition and microstructure of were investigated by transmission electron microscope (TEM, Tecnai  $G^2$  F20) equipped with a high-angle angular-dark-field detector and Xray energy-dispersive spectrometer systems.

Fabrication of TENG and Schottky junction: A 4-mm acrylic plate served as the robust substrate. Aluminum foil and Kapton membrane were chosen as friction layers to generate high voltage. The size of aluminum foil and Kapton film is 8 cm × 8 cm. The Cu film with the thickness of 50 nm was deposited on the back side of Kapton film with the thickness of 100 µm is used as the electrode. Copper electrodes with a spacing of 10 μm were deposed on the substrate of amorphous  $SiO<sub>2</sub>$  by UVlithography technology. CuO nanowires were ultrasonically dispersed in ethanol. CuO nanowires were aligned on the Cu electrode array by electric-field assisted assembly and alignment. The voltage of 12 V with the frequency of 50 kHz is generated by the signal generator (ArbStudio 1104) to produce an [alternating](https://fanyi.baidu.com/#en/zh/alternating%20current%20electric%20field) electric field. Pt electrode was deposited on both sides of CuO nanowires by the technology of focus ion beam (FIB, FEI Scios 2) to form Schottky contact. Nanoscale **Accepted Conservation** on 13 September 2022. The conservation of the energy and the energy and the energy and Nanoscale Conservation of Nanoscale Conservation of Nanoscale Conservation of Nanoscale Scheme Conser

Electrical Measurements: Voltage of TENG was measured by Tektronix oscilloscope (Type: HD06104). *I-V* curve ranged from -5 V to 5 V was recorded by semiconductor characterization system (Keithley 4200-SCS).

#### **Results and discussion**

CuO nanowire is synthesized by the vapor-solid method. It is grown by heating a copper grid in air. Copper gride used in transmission electron microscopy is cleaned in an aqueous 1.0 M HCl solution for ∼20 s. Afterward, it was rinsed with distilled water repeatedly, following by drying under a N<sub>2</sub> gas flow. Copper gride was heated for 10 h in a tube furnace with a temperature of 500 ºC immediately (Figure 1a-d). CuO nanowires with the length ranged from 10 μm to 30 μm are obtained (Figure 1e). XRD pattern indicates that CuO nanowires are composed of  $CuO$  and  $Cu<sub>2</sub>O$ . When copper gride is heated in air, the major product is  $Cu<sub>2</sub>O$ . CuO is synthesized slowly through the oxidation of  $Cu<sub>2</sub>O$  further. In this case,  $Cu<sub>2</sub>O$  is the precursor to the formation of CuO. The formation of CuO vapor from  $Cu<sub>2</sub>O$  is rate-determining step  $33$ .

$$
4Cu + O_2 \rightarrow 2Cu_2O \tag{1}
$$

$$
2Cu2O + O2 \rightarrow 4CuO
$$
 (2)

Vapor pressure for CuO in the chamber is low due to the slow rate for the formation of CuO. It ensures a continuous growth mode and uniform diameter for the CuO nanowires. Additionally, the slow growth rate is beneficial to improve the crystallinity of CuO nanowire.



Figure 1. Synthesis steps and characterization of CuO nanowire. (ad) The synthesis process of CuO nanowires. (e) SEM image of CuO nanowires. (f) XRD pattern of CuO nanowires.

TEM method is utilized to characterize the morphology and microstructure further. The diameter of prepared CuO nanowires is about 150 nm (Figure 2a). The regular arrangement of atoms observed from high-resolution TEM (HRTEM) image exhibits the nature of high crystallinity (Figure 2b). Fluctuation in the arrangement of the atoms is owing to slight stacking faults in CuO nanowires. The interplanar distances measured from the HRTEM image are 2.496 Å, 2.306 Å, 2.275 Å, respectively, which are assigned to the panels of  $(\frac{1}{1}11)$ , (111), and (200) in the CuO nanowires. The two sets of diffraction spots are observed in the SAED image (Figure

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2c) confirming the existence of stacking faults within the CuO nanowire. The calibration of diffraction spots is corresponding to the diffraction of the lattice plane in CuO. EDS spectrum of a single CuO nanowire is shown as the red (Cu) and green (O) color (Figure 2e-f). The clear signal of Cu and O elements is further evidence of CuO nanowire.



Figure 2. Structural information of CuO nanowire. (a-c) TEM image, HRTEM, and SAED image of CuO nanowire. (d-f) EDS element mappings of Cu, O.

The voltage of TENG after rectification is applied on Pt-CuO-Pt Schottky diode (Figure 3a). The corresponding equivalent circuit is shown in figure 3b.  $R_{nw}$  is the resistance of nanowire.  $R_{sh1}$  and  $R_{sh2}$ are the shunt resistances associated with the two Schottky barriers 34, 35. The output voltage of TENG after rectification is about 180 V with a frequency of 1 Hz (Figure 3c). The device exhibits the nonlinear *I-V* curve, which indicates the Schottky junction is formed between Pt and CuO nanowire at both ends (Figure 3d). The almost symmetrical *I-V* curve infers the approaching Schottky barrier height. The inset image at the top of figure 3d is the SEM image of the Pt-CuO-Pt Schottky diode. The almost same area of Pt and uniform performance of CuO nanowire makes the formation of approaching Schottky barrier height. According to the classic thermionic emissiondiffusion theory (V>>3kT/q~77 mV),<sup>36,37</sup> the current of the Schottky diode can be determined as follow formulae Nanoscale **Accepted on 13 September 2021.** The method of *Published Case of Conservergies* Conservergies Conservergies Conservergies Conservergies Conservergies Conservergies Conservergies Conservergies Conservergies Con

$$
I_R = SA^*T^2 \exp(-\frac{\phi_{B0}}{kT}) \exp\left(\frac{\sqrt[4]{q^7 N_D (V + V_{bi} - kT/q)/(8\pi^2 \varepsilon_s^3)}}{kT}\right)
$$
 (1) The conduction and valence bands bend of  
barrier height is increased after the treatr

in which *S* is the contact area between metal and semiconductor,  $A$ \* is the effective Richardson constant, the value is 14.5 A/cm<sup>2</sup>K<sup>2</sup>,<sup>38</sup>  $T$  is the absolute temperature,  $\phi_{B0}$  is the Schottky barrier height, in the atomic str

 $k$  is the Boltzmann constant,  $q$  is the unit electronic charge,  $\mathcal{W}_{\text{D}}$  linis the doping concentration,  $V$  is the applied voltage,  $V_{bi}$  is the buildin potential,  $\mathcal{E}_{s}$  is the permittivity of CuO. The Schottky barrier height is obtained by plotting the curve of  $\ln$  (*I*) and  $V^{1/4}$ . Schottky barrier heights of drain and source are 0.56 and 0.55 eV after calculation. The inset image at the bottom of figure 3d is a schematic of the Pt-CuO-Pt Schottky diode.



Figure 3. Diagram of experiment setup and basic performance of TENG and Pt-CuO-Pt Schottky device. (a) Schematic illustration of the experiment setup. (b) equivalent circuit corresponding to the figure a. (c) Open-circuit voltage of TENG. (d) *I-V* curve of Pt-CuO-Pt Schottky device, the inset on the top is the SEM image of Pt-CuO-Pt, the inset on the bottom is the schematic structure of the device.

 $= SA^*T^2 \exp(-\frac{\phi_{B0}}{2\pi}) \exp\left(\frac{\sqrt[4]{q^7 N_D (V+V_{bi}-kT/q)/(8\pi^2 \varepsilon_s^3)}}{1-\frac{1}{2\pi})}\right)$  (1) The conduction and valence bands bend downwards. The Schottky  $\sum_{R}$  =  $SA*T^2$  exp( $-\frac{\phi_{B0}}{kT}$ ) exp $\left(\frac{\sqrt[4]{q^7}N_D(V+V_{bi}-kT/q)/(8\pi^2\varepsilon_3^3)}{kT}\right)$  (1) The conduction and valence bands being downwards. The schoting *I-V* curve before and after TENG treatment is shown in Figure 4a. The current of the Pt-CuO-Pt Schottky diode is decreased after TENG treatment, additionally, the decrease amount is enhanced with the increase of treatment times. Schottky barrier heights of source and drain are raised from 0.55 and 0.56 eV to 0.59 and 0.60 eV (Figure 4b). Schottky barrier height increases monotonously by increasing times of TENG treatment. When Pt and CuO nanowire are contacted, the Schottky barrier (hole barrier) is formed because of (ⅰ) the difference of the work function of Pt and the ionization energy of CuO nanowire, and (ⅱ) surface state of CuO nanowire (Figure 4c). TENG (Figure 4d). It is well known that the native oxygen defect is generated inevitably during the synthetic process of CuO nanowires because of low formation energy <sup>26</sup>. It is showed as the black spheres in the atomic structure of CuO (Figure 4c). Defect in CuO nanowire

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creates defect localized states. The interaction of the external electric field and localized state around the oxygen defect happens once the electric field is applied. Electrical potential induced by the external electrical field will be proportionally and directly converted into energy gain in CuO nanowires associated with local lattice relaxations. Oxygen vacancy is charged under the stimulation of electric field from the out-put voltage of TENG. It converts into ionized oxygen vacancy ( $V_O^0 \rightarrow e + V_O^+$  or  $V_O^+ \rightarrow e + V_O^{2+}$ ).<sup>39, 40</sup> Ionized oxygen vacancies are diffused toward the interface of Pt and CuO nanowire. The ionized oxygen vacancy with positive charge accumulated at the interface.<sup>41</sup> It acts as the positive "gate" voltage, which leads to the conduction and valence bands of CuO around the interface bend downwards simultaneously. The Schottky barrier height formed by Pt and CuO nanowire is raised.



Figure 4. Change of Schottky barrier height and polarization model of Pt and CuO (p-type) interface to Pt-CuO-Pt. (a) *I-V* curves of the Pt-CuO-Pt device under different times of TENG treatment. (b) the corresponding change of Schottky barrier height at source and drain. (c) Schematic atomic structure of the Pt-CuO device, band diagram of Schottky contacted Pt-CuO (p-type) interface at initial state. (d) The oxygen vacancies are driven by the positive high voltage of TENG, and they are accumulated at the junction around the interface.

#### **Conclusions**

In summary, we investigate the influence of impulse voltage of TENG on the Schottky barrier height of Pt-CuO-Pt. First, the

 $V_0^+$   $\rightarrow$   $e + V_0^{2+}$  ).<sup>39,40</sup> based Schottky junction is investigated. The Schottky barrier uniform CuO nanowires  $(-150 \text{ nm})$  in diameter) with lengths ranged from 10 to 30 μm is synthesized by the vapor-solid method. The stacking fault is found in CuO nanowire by analysis of HRTEM and SAED. Then, the Schottky-contacted device based on CuO nanowires and Pt is fabricated by electric-field assisted alignment and focus ion beam technology. The interaction of the output voltage from TENG and CuO nanowireheight is raised after the treatment by the voltage of TENG. A theoretical model based on the energy band diagram is proposed to explain the phenomenon of the change of Schottky barrier height. The oxygen vacancy is transferred into the ionized oxygen vacancy under the impact of voltage from TENG. The ionized oxygen vacancy is diffused toward the contact interface of Pt and CuO nanowire, which leads to the increase of Schottky barrier height. This work is beneficial to understand the tuning effect of Schottky barrier height in depth. This method opens up the possibility to develop a high-performance Schottky sensor by modulating Schottky barrier height. Nanote the accepted on 13 September 2021. The method of Nanote Beijing Institute of Nanoscale Published on 13 September 2021. The method of Nanoscale Published online Case and Published Case and Published Case and Publishe

#### **Author contributions**

Jianping Meng: Conceptualization, investigation, methodology, writing-original draft, data curation, validation, funding acquisition. Qi Li: Investigation, data curation. Jing Huang: Investigation. Zhou Li: Supervision, resources, funding acquisition, writing - review & editing. All authors writingreview & editing.

#### **Conflicts of interest**

There are no conflicts to declare.

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